

Figures

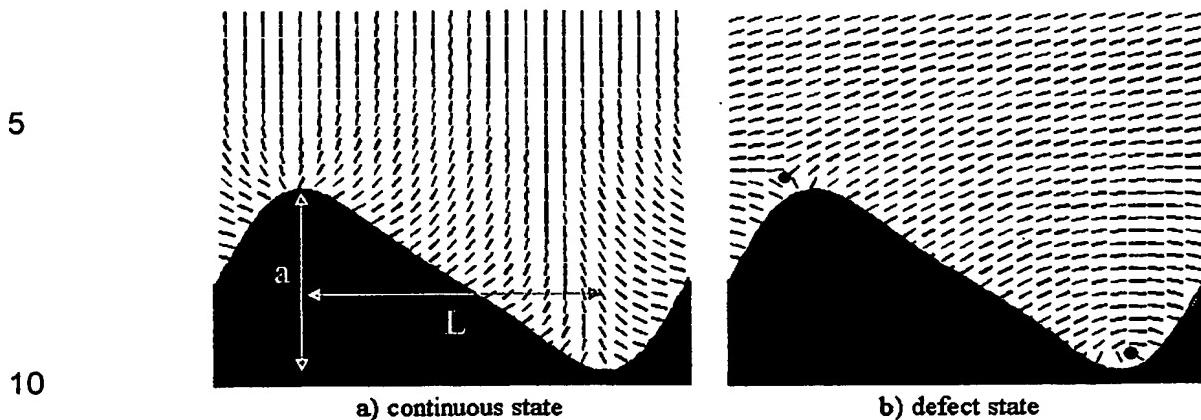


Figure 1. Illustration of zenithal bistability using grating alignment (with the lines indicating the local director) showing a) the high tilt (continuous) and b) the low tilt (defect) states.

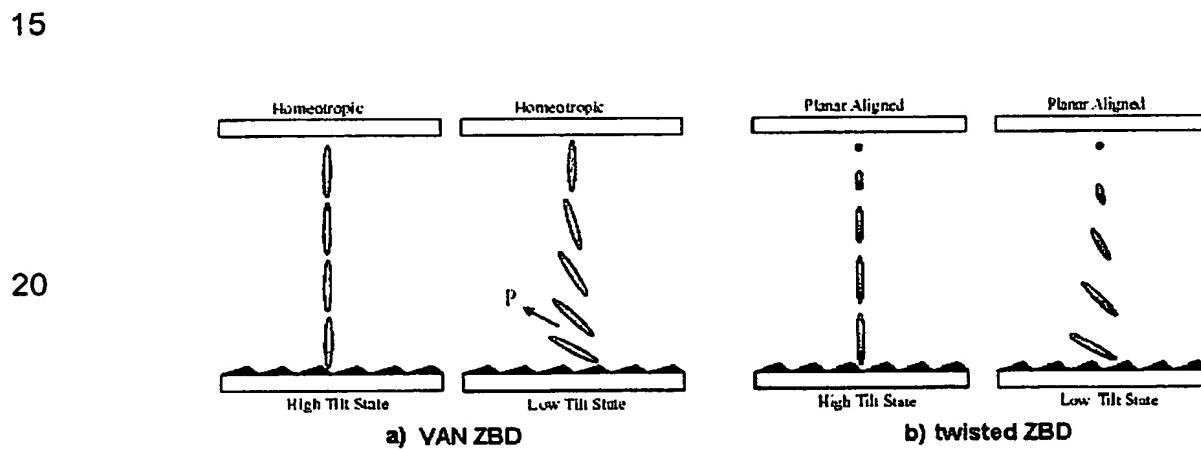


Figure 2. The a) VAN and b) TN geometries for ZBD showing the director distribution of the high and low tilt states.

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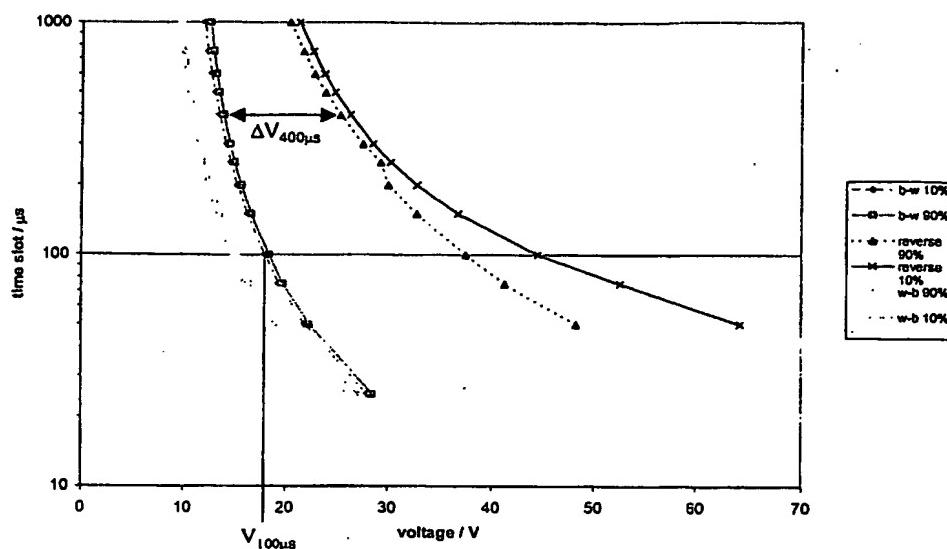


Figure 3. The τ -V curve for MLC-6204-000 at 25°C (pulse duration or time slot vs. voltage). The switching voltage ($V_{100\mu s}$) and operating window ($\Delta V_{400\mu s}$) used for comparisons are indicated.